


Search Notes 	Application/Control No. 10796499	Applicant(s)/Patent Under Reexamination ESPOSITO, BEN
	Examiner Alrobaye, Idriss N	Art Unit 2183

SEARCHED			
Class	Subclass	Date	Examiner
712	35		

SEARCH NOTES		
Search Notes	Date	Examiner
Inventor Search (double patenting)	01-10-2007	IA
See East Search history	01-10-2007	IA
NPL (google; terms used: array register column multiplexer or mux, matrix row registers, dsp or digital signal processing, tap or tapped delay line or delay circuit)	01-11-2007	IA
Consulted WQAS Mano with regards to the final action, even though a first final action was sent	05/01/2008	IA
See East search history	4/18/2008	IA

INTERFERENCE SEARCH			
Class	Subclass	Date	Examiner